

SYED UMAIR SHOAIIBY

Senior Technical Program Manager | AI Infrastructure & PMO Transformation | Semiconductor NPI
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EXECUTIVE SUMMARY

- Senior Technical Program Manager with 14 years of experience leading complex, cross-functional engineering programs overseeing NPI processes and system-level product development from NPI and test chip through multi-chip modules to high volume manufacturing at scale.
- Core member of Cirrus Logic's enterprise AI Infrastructure team supporting deployment of internal AI tooling environment, defining deployment strategies and enabling adoption of AI powered workflows across engineering and PMO functions organization wide.
- Experienced in direct customer engagement, partnering with sales, engineering, and product teams to define solution strategies, align stakeholders, and deliver high-impact outcomes. Strong background in bridging hardware and software domains, including firmware, system validation, and OS-level integration.
- Strong hardware technical background in device Validation/Characterization, statistical analysis, yield enhancement, and failure analysis.
- Certified in CAPM, CSM Scrum Master, PMD and Gen AI for PM (PMI); actively pursuing PMI CPMAI to become certified professional in managing AI.

PROFESSIONAL EXPERIENCE

Senior Technical Program Manager (Post-Silicon) at CIRRUS LOGIC | Austin, Texas | *April 2022 — Present*

AI Infrastructure & PMO Transformation Leadership

- Participate in the enterprise AI infrastructure team as a core member, co-leading the architecture, deployment, and scaling of Cirrus Logic's internal AI automation environment. This includes the integration of Glean AI, Microsoft Copilot, Claude Code and ChatGPT/Codex into daily engineering and PMO workflows.
- Conducting end-to-end process audits, redesigning workflows, and deploying updated frameworks that eradicate bottlenecks and reduce program cycle times through systematic AI integration, leading the modernization of PMO methodology across the organization.
- Championing AI-augmented program management practices: embedding AI tools into risk identification, executive status reporting, resource and schedule planning, and stakeholder communication to improve responsiveness and reduce manual overhead.

Semiconductor Program Execution

- Led end-to-end execution of complex semiconductor programs spanning Codecs, Amplifier, camera chip, and sensor solutions; delivering products from test chip through multi-chip modules to high-volume production, generating multi-billion-dollar revenue.
- Acted as primary bridge between customers, sales, business development, and engineering teams translating customer requirements into scalable system-level technical solutions and driving alignment across all stakeholder groups.
- Managed global cross-functional teams across design, validation, product engineering, OSAT, and manufacturing to ensure on-time product delivery against aggressive milestones and customer commitments.
- Drove data-driven risk management, program roadmap planning, and mitigation strategies across validation, Software, Design support, characterization, and production phases.

Senior Product Engineer at INFINEON TECHNOLOGIES | Los Angeles, California | August 2018 — March 2022

- Delivered multiple high-impact semiconductor products (codec audio, DC-DC converters, multi-chip modules) from development to production under aggressive timelines.
- Successfully qualified products with aggressive customer production ramp schedules and data analysis (ESD/LU, Device/Package Qualification and ATE Characterization)
- Drove process improvements using structured methodologies (8D, RCA), enhancing product reliability and reducing defects.
- Led Product Engineering group in defining guard-banding techniques and standardizing the flow across departments.
- Identified and resolved process gaps through lessons learned, improving stress board development and characterization methodologies.
- Worked closely with Project Managers in scheduling, risks and cost assessments. Completed PMD certification during the process.

Product Engineer (Contract) at QUALCOMM | San Diego, California | April 2017 — July 2018

- Conducted full device characterization, statistical analysis, and yield enhancement for high-volume semiconductor products.
- Utilized advanced data tools (DataPower, Gauge R&R, drift analysis) to improve product performance and consistency.
- Supported customer returns (RMA) and collaborated with cross-functional teams to resolve field issues.
- Worked with internal and external groups (design, test, QA, process, marketing) to resolve issues across global cultures and different regions and time zones.

NPI Product Engineer at BROADCOM LIMITED | Irvine, California | May 2013 — March 2017

- Successfully completed 10G PHY NPI product qualification and production ramp.
- Optimized yield enhancement for 28nm and 40nm CMOS process technologies to meet cost model expectations for high volume wireless products; Example: 97+% blind build yield
- Efficiently qualified products at multiple foundries concurrently to meet demands based on capacity and cost reduction models; Example: GF, SMIC, TSMC and UMC etc.
- Managed cross-functional efforts in test time reduction, yield enhancement, and process integration.

CERTIFICATIONS & TOOLS

- **Certifications:** CAPM (PMI) | Certified Scrum Master | Program Management Development | Gen AI for PM (PMI) | PMI Certified Professional in Managing AI (In Progress)
- **PM Tools:** JIRA | Confluence | SharePoint | Smartsheet | Clarity
- **AI:** ChatGPT | Claude Code & Cowork | Microsoft Copilot | Glean AI | Agentic AI
- **Statistical Analysis:** JMP | DataPower | DataConductor | Gauge R&R | Drift Analysis

EDUCATION

- **M.S. Electrical Engineering** California State University, Long Beach | GPA: 3.7
- **B.S. Electronic Engineering** Sir Syed University, Karachi, Pakistan | GPA: 3.5